QUAID-E-AWAM UNIVERSITY OF ENGINEERING, SCIENCE & TECHNOLOGY, NAWABSHAH.

The Quaid-e-Awam University of Engineering, Science & Technology announces the result of **Regular** Examination of **First Term – Third Year** (5th Term) Bachelor of Engineering (Electronic) 10-Batch held in May / June – 2012 as under:

BACHELOR OF ENGINEERING (ELECTRONIC) MAXIMUM MARKS: 700

Candidates having the following Identification Numbers (I.D. Nos.) are declared successful.

ID No. 10ES01(589), 02(547), 03(498), 04(519), 05(570), 06(546), 07(532), 08(495), 09(450), 10(504), 11(543), 12(521), 14(515), 15(512), 16(414), 18(588), 19(486), 20(454), 21(471), 22(463), 23(439), 24(370-APR), 26(474), 27(425), 28(498), 30(554), 31(533), 32(397), 34(467), 35(466), 37(521), 38(421), 39(436), 40(453), 41(520), 43(393-APR), 44(577), 45(377-APR), 46(371-APR), 47(469), 51(492), 52(452), 53(443-APR), 54(361-APR), 55(472)

LIST OF FAILURES

I.D No.	SUBJECTS
10ES17	Probability & Random Variables, Signals & Systems (Th)
10ES33	Control Systems (Th)
10ES36	Signals & Systems (Th), Control Systems (Th), Integrated Circuit Design & Applications (Th)
10ES42	Failing in all subjects except Signals & Systems (Pr)
10ES48	Embedded System Design (Th), Probability & Random Variables, Signals & Systems (Th), Control Systems (Th⪻), Integrated Circuit Design & Applications (Th)
10ES49	Embedded System Design (Th), Integrated Circuit Design & Applications (Th)
10ES50	Embedded System Design (Th), Probability & Random Variables, Signals & Systems (Th), Control Systems (Th⪻), Integrated Circuit Design & Applications (Th)
10ES56	Probability & Random Variables, Signals & Systems (Th), Control Systems (Th), Integrated Circuit Design & Applications (Th)
10ES57	Integrated Circuit Design & Applications (Th⪻)
10ES58	Embedded System Design (Th), Probability & Random Variables, Signals & Systems (Th⪻),
	Control Systems (Th⪻), Integrated Circuit Design & Applications (Th)
10-09ES29	Embedded System Design (Th), Probability & Random Variables, Signals & Systems (Th),
	Control Systems (Th), Integrated Circuit Design & Applications (Th)

NOTE: The University reserves the right to correct any error, mistake or omission in the results or certificates at any time upon verification of the record.

CONTROLLER OF EXAMINATIONS

Nawabshah Dated **04-12-2012**