



Programme of Supplementary Examination (Practical) of First Term-Second Year (3rd Term) Bachelor of Engineering (Civil, Mechanical, Electrical, Computer Systems, Electronic, Telecommunication, Energy & Environment and Chemical) 08 to 14-Batches (Under External Examiners' System) to be held from Monday, the 26th September, 2016

PRACTICAL EXAMINATION

Note: However, this shall be the **LAST CHANCE** for the eligible failure students of **08-Batch** and for those students who were initially admitted with **08-Batch** but were re-admitted with lower batches

Day, Date & Time	Civil	Mechanical	Electrical	Computer Systems	Electronic	Telecommunication	Energy & Environment	Chemical
Monday 26.09.2016 5:10 p.m. and onwards	Engineering Geology (08 to 11-Batches)	Engineering Materials (08 to 10-Batches) Electronic Engineering (11 to 14-Batches)	Instrumentation & Measurements (08 to 10-Batches) C++ Programming (11 to 14-Batches)	Electronic Engineering-II (08 to 11-Batches)	-----	-----	-----	Unit Process
Tuesday 27.09.2016 5:10 p.m. and onwards	-----	-----	-----	-----	Amplifiers & Oscillators	Amplifiers & Oscillators	Thermodynamics-I	-----
Wednesday 28.09.2016 5:10 p.m. and onwards	Civil Engineering Drawing	Computer Systems & Programming (11 to 14-Batches)	Linear Electric Circuits (08 to 10-Batches) Linear Circuit Analysis (11 to 14-Batches)	Linear Electric Circuits (08 to 11-Batches) Database Management Systems (12 to 14-Batches)	Linear Circuit Analysis/ Circuit Analysis	Circuit Analysis	-----	Basic Electrical Technology
Thursday 29.09.2016 5:10 p.m. and onwards	Strength of Materials-I (08 to 11-Batches)	Strength of Materials-I (08 to 10-Batches) Mechanics of Materials-I (11 to 14-Batches)	Engineering Mechanics-II (08 to 10-Batches) Applied Electronics (11 to 14-Batches)	Data Structures & Algorithms / Analysis	Digital Electronics (08-Batch) Digital Logic Design (09 to 14-Batches)	-----	Computer Systems & Programming	-----
Friday 30.09.2016 5:40 p.m. and onwards	Surveying-II	Thermodynamics-I	Applied Thermodynamics	Digital Logic & Design	-----	Digital Logic Design	-----	-----
Saturday 01.10.2016 5:10 p.m. and onwards	-----	-----	-----	-----	-----	-----	-----	Chemical Engg: Thermodynamics-I

* contents are same

Note: - (i) Eligible candidates are required to present themselves at the place of examination 15 minutes before the time fixed for examination (ii) Eligible candidates are required to bring their ADMIT SLIPS at the time of Examination. The candidates without ADMIT SLIPS shall not be allowed to appear in the Examination. (iii) The appearance of IN-ELIGIBLE candidates in the Examination shall be illegal and as such his / her result of concerned subject(s) will automatically stand cancelled without issuing any letter. (v) All eligible candidates MUST refrain from bringing any sort of written material & Mobile Phone in the Examination Hall/Room during the conduct of Examination. (iv) Eligible candidates MUST read carefully the INSTRUCTIONS written on ADMIT SLIP for strict compliance.

Copy to All concerned


CONTROLLER OF EXAMINATIONS