

QUAID-E-AWAM UNIVERSITY OF ENGINEERING, SCIENCE & TECHNOLOGY, NAWABSHAH

The Quaid-e-Awam University of Engineering, Science & Technology announces the result of **Regular Examination of First Semester – Second Year (3rd Semester) Bachelor of Engineering (Electronic) 18-Batch** held in March / April – 2019 as under:

BACHELOR OF ENGINEERING (ELECTRONIC)

Candidates having the following Identification Numbers (I.D. Nos.) are declared successful in GPA.

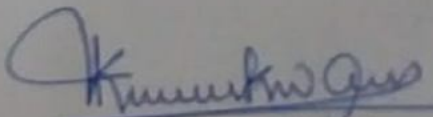
ID NO. 18ES01(03.88), 03(03.36), 04(03.43), 06(03.90), 08(03.91), 11(03.50), 12(03.43), 16(04.00), 17(03.90), 19(03.41), 20(03.68), 21(03.45), 24(03.16), 32(03.75), 34(03.18), 39(03.20), 40(03.56), 42(02.87), 47(03.83), 48(03.52), 49(03.77), 51(03.37), 53(03.16), 54(03.58)

LIST OF FAILURES

I.D No.	SUBJECTS
18ES07	Instrumentation & Measurements (Th)
18ES09	Digital Logic Design (Th), Instrumentation & Measurements (Th), Electronic Circuit Design (Th&Pr)
18ES13	Instrumentation & Measurements (Th)
18ES18	Instrumentation & Measurements (Th)
18ES22	Digital Logic Design (Th&Pr), Instrumentation & Measurements (Th), Electronic Circuit Design (Th)
18ES23	Digital Logic Design (Th), Instrumentation & Measurements (Th), Electronic Circuit Design (Th)
18ES27	Digital Logic Design (Th), Instrumentation & Measurements (Th), Electronic Circuit Design (Th)
18ES28	Digital Logic Design (Th), Instrumentation & Measurements (Th), Electronic Circuit Design (Th)
18ES29	Digital Logic Design (Th), Instrumentation & Measurements (Th), Electronic Circuit Design (Th)
18ES30	Digital Logic Design (Th), Instrumentation & Measurements (Th), Electronic Circuit Design (Th)
18ES31	Instrumentation & Measurements (Th)
18ES35	Digital Logic Design (Th)
18ES36	Digital Logic Design (Th), Instrumentation & Measurements (Th)
18ES37	Instrumentation & Measurements (Th)
18ES38	Differential Equations & Fourier Series, Digital Logic Design (Th), Instrumentation & Measurements (Th), Electronic Circuit Design (Th)
18ES41	Digital Logic Design (Th), Instrumentation & Measurements (Th), Electronic Circuit Design (Th)
18ES43	Digital Logic Design (Th), Instrumentation & Measurements (Th), Electronic Circuit Design (Th)
18ES44	Digital Logic Design (Th), Instrumentation & Measurements (Th), Electronic Circuit Design (Th)
18ES46	Electronic Circuit Design (Th)
18ES50	Digital Logic Design (Th), Instrumentation & Measurements (Th)
18ES55	Instrumentation & Measurements (Th)
18ES56	Digital Logic Design (Th), Instrumentation & Measurements (Th)

NOTE: The University reserves the right to correct any error, mistake or omission in the results or certificates at any time upon verification of the record.

Nawabshah Dated: 05.09.2019


CONTROLLER OF EXAMINATIONS