

**QUAID-E-AWAM UNIVERSITY OF ENGINEERING, SCIENCE & TECHNOLOGY, NAWABSHAH**

The Quaid-e-Awam University of Engineering, Science & Technology announces the result of **Regular Examination of First Semester – Second Year (3<sup>rd</sup> Semester) Bachelor of Engineering (Electrical) 17-Batch** held in April – 2018 as under:

**BACHELOR OF ENGINEERING (ELECTRICAL)**

Candidates having the following Identification Numbers (I.D. Nos.) are declared successful in GPA.

**ID NO.** 17EL01(04.00), 03(02.15), 04(03.17), 05(01.65), 08(01.79), 09(03.62), 10(03.57), 11(04.00), 12(03.67), 13(03.60), 14(02.46), 15(02.42), 18(03.95), 20(03.67), 23(03.62), 24(03.34), 26(03.43), 27(03.95), 28(03.51), 29(03.90), 30(01.73), 31(02.92), 32(03.56), 33(01.84), 35(03.95), 36(03.85), 38(03.78), 39(03.20), 40(02.31), 41(03.70), 43(03.60), 44(03.67), 45(02.53), 46(02.31), 47(03.57), 48(03.62), 49(03.15), 50(03.23), 52(01.71), 53(02.59), 54(01.71), 55(03.07), 57(03.90), 58(01.90), 59(03.06), 61(02.82), 62(03.85), 63(03.34), 64(03.45), 65(03.81), 67(04.00), 68(03.20), 69(02.64), 74(02.03), 75(03.15), 76(03.10), 77(02.45), 79(02.87), 89(03.71), 98(03.32), 99(02.92), 100(02.68), 101(03.85), 102(02.21)

**LIST OF FAILURES**

<b>I.D No.</b>	<b>SUBJECTS</b>
17EL06	Electrical Network Analysis, Digital Logic Design (Th), Differential Equation & Fourier Series, Applied Electronic (Th&Pr), Applied Thermodynamics
17EL07	Electrical Network Analysis, Digital Logic Design (Th), Applied Electronic (Th)
17EL16	Differential Equation & Fourier Series, Applied Electronic (Th), Applied Thermodynamics
17EL17	Electrical Network Analysis, Differential Equation & Fourier Series
17EL22	Electrical Network Analysis, Differential Equation & Fourier Series
17EL51	Electrical Network Analysis, Differential Equation & Fourier Series
17EL56	Electrical Network Analysis, Digital Logic Design (Th), Differential Equation & Fourier Series, Applied Electronic (Th), Applied Thermodynamics
17EL60	Differential Equation & Fourier Series
17EL70	Electrical Network Analysis, Differential Equation & Fourier Series
17EL71	Electrical Network Analysis, Differential Equation & Fourier Series
17EL72	Electrical Network Analysis, Digital Logic Design (Th), Differential Equation & Fourier Series, Applied Electronic (Th)
17EL73	Electrical Network Analysis, Digital Logic Design (Th), Differential Equation & Fourier Series
17EL78	Digital Logic Design (Pr)
17EL81	Electrical Network Analysis, Digital Logic Design (Th), Differential Equation & Fourier Series, Applied Electronic (Th&Pr)
17EL82	Differential Equation & Fourier Series
17EL83	Digital Logic Design (Pr)
17EL84	Electrical Network Analysis, Digital Logic Design (Pr), Differential Equation & Fourier Series
17EL85	Electrical Network Analysis, Digital Logic Design (Th&Pr), Differential Equation & Fourier Series
17EL86	Electrical Network Analysis, Digital Logic Design (Th&Pr), Differential Equation & Fourier Series, Applied Thermodynamics
17EL87	Digital Logic Design (Pr), Differential Equation & Fourier Series
17EL90	Differential Equation & Fourier Series
17EL91	Failing in all subjects

*Continued on P/2*

17EL92	Electrical Network Analysis, Differential Equation & Fourier Series
17EL93	Failing in all subjects except Applied Electronic (Pr)
17EL94	Electrical Network Analysis (Th), Digital Logic Design (Th&Pr), Applied Electronic (Th)
17EL95	Electrical Network Analysis (Th), Digital Logic Design (Th&Pr), Differential Equation & Fourier Series, Applied Electronic (Th), Applied Thermodynamics
17EL96	Digital Logic Design (Th&Pr)
17EL97	Digital Logic Design (Pr)
17EL103	Failing in all subjects except Applied Electronic (Pr)

**NOTE:** The University reserves the right to correct any error, mistake or omission in the results or certificates at any time upon verification of the record.

Nawabshah Dated: 03.09.2018

  
CONTROLLER OF EXAMINATIONS